

General	Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Name of the board: <b>Bubblegum96</b> Email address (primary): <b>huhong@ucrobotics.com</b> Email address (secondary): <b>yueming.wang@octonov-sem.com</b> Target Specification: <b>CE v1.0</b> Form Factor: <b>CE v1.0 (standard) - 85 x 54 mm</b>	<b>+/ 0.25mm error margin allowed</b>				Using 3D templates for the appropriate form factor
<b>Key</b>	<b>Documentation</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Board schematic published: <b>Yes</b> Board Reference Manual published: <b>Yes</b> SoC technical reference published: <b>Yes</b>	<a href="#">Board reference manual should follow template</a> <a href="#">SoC TBM published as per guidelines</a>		<b>Fail</b>	<b>Technical reference manual is not</b>	<b>Pass</b>
<b>Key</b>	<b>Memory and Storage</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Onboard DRAM (GB): <b>2</b> SD Card: <b>microSDHC</b> Boot from SD Card: <b>Yes</b> Boot ROM: <b>No</b> Boot ROM size (MB): <b>8</b> Onboard Flash storage: <b>Yes</b> Onboard Flash storage size (GB): <b>Optional</b> SATA interface: <b>None</b>	CE: $\Rightarrow$ 0,5GB; EE: $\Rightarrow$ 1GB or DIMM EE: Mandatory if no onboard DRAM CE: Mandatory CE: optional, only if SD card is not bootable; EE: mandatory CE: BMB (optional); EE: 64 MB (mandatory) Optional Optional Optional			#VALUE! #VALUE!	
<b>Key</b>	<b>Networking</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	WiFi protocol: <b>802.11 g/n</b> Bluetooth: <b>4.1 LE</b> <b>Yes</b> Ethernet: <b>No</b>	EE: mandatory				
<b>Key</b>	<b>Debugging</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	UART 0: <b>No</b> UART 1 (L5 expansion interface): <b>Yes</b> UART 2 (L5 expansion interface): <b>Yes</b> 4 User LEDs: <b>Yes</b> WiFi LED: <b>No</b> BT LED: <b>No</b> JTAG: <b>Yes</b>	EE: micro B USB in specified location default mandatory UART CE: optional; EE: NA Optional				
<b>Key</b>	<b>USB</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Port 1 - Host (Protocol): <b>USB 3.x</b> Port 1 - Host (Connector): <b>Type A</b> Port 2 - Host (Protocol): <b>USB 2.x</b> Port 2 - Host (Connector): <b>Type A</b> Port 3 - Slave (Protocol): <b>USB 2.x</b> Port 3 - Slave (Connector): <b>micro-AB (for OTG)</b> Port 4 - Host (Protocol): <b>USB 2.x</b> Port 4 - Host (Connector): <b>Other</b>	EE: Host Port 1 & 2 are stacked connectors EE: Host Port 1 & 2 are stacked connectors				
<b>Key</b>	<b>Display</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Display Type: <b>HDMI</b> Display Connector: <b>Type A (full-size)</b> MIPi DSI port on HS expansion bus: <b>Yes</b> MIPi DSI lanes: <b>4</b>	CE: Audio support for at least 1 channel is mandatory				
<b>Key</b>	<b>Camera</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	MIPi CSI-2 ports: <b>4</b>	(Review) CE: Port 1 should be on CSI port (1-4 lanes), CSI port supports 1-2 lanes				
<b>Key</b>	<b>Audio</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Audio channels (BT): <b>2</b> Audio channels (Display): <b>2</b>	CE: I2S/PCH channel should be provided on L5 expansion interface				
<b>Key</b>	<b>DC Power</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Power source: <b>DC Power Jack (9-18V)</b>					
<b>Key</b>	<b>Buttons / Switches / Jumpers</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Power on/off: <b>Yes</b> Hard reset: <b>Yes</b> Auto power up: <b>Through Switch</b> USB mode change: <b>NA</b>	Mandatory Mandatory Mandatory Optional, to change mode of the OTG port				
<b>Key</b>	<b>Expansion header</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Low-speed expansion header: <b>Yes</b> High-speed expansion header: <b>Yes</b>	Signals to be exposed as per spec CE: mandatory; EE: NA, Signals to be exposed as per spec				
<b>Key</b>	<b>Miscellaneous</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	External Fan: <b>None</b>	EE: location specified but optional				
<b>Key</b>	<b>Software</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Kernel git tree URL: <a href="https://git.linaro.org/people/paul.liu/vendor-kernel.git">git://git.linaro.org/people/paul.liu/vendor-kernel.git</a> Kernel version: <b>v3.10</b> Kernel compliance Level: <b>Other</b> All bootloader bits are open source: <b>No</b> Last-stage bootloader git tree URL: <a href="https://git.linaro.org/people/paul.liu/vendor-uboot.git">git://git.linaro.org/people/paul.liu/vendor-uboot.git</a> Fastboot protocol support: <b>Yes</b> User can update bootloader: <b>Yes</b> Possibility to unlock the board w/o special hardware: <b>Yes</b> CPU acceleration: <b>Needs userspace binary blobs</b> Camera ISP: <b>Needs userspace binary blobs</b> DSP: <b>NA</b> Multimedia: <b>Needs userspace binary blobs</b>	Level definitions as per spec According to the spec, everything after SoC reset should be available as open source e.g. USB/BT/ZX or v-boot	<b>Fail</b>	<b>3.10 doesn't correspond to the lin</b>		
<b>Key</b>	<b>Software Functionality</b>	<b>Response</b>	<b>Comments</b>	<b>Validation Results (Linaro)</b>	<b>Linaro comments</b>	<b>Final Results (Linaro)</b> <b>Validation Test Description</b>
	Boot to Graphical UI: <b>Yes</b> Graphics acceleration using SoC GPU: <b>Yes</b> Standard set of Distribution end-user applications: <b>Partial</b> Video playback support for media files: <b>Yes</b>					

