Key	General	Response	Comments	Validation	Linaro comments	Final Posults (Linasa)	Validation Test Description
Key			Comments	Results (Linaro)	Linaro comments	rinat Results (Linaro)	validation rest Description
	Name of the board	Mediatek X20					
	Email address (primary)						
	Email addresses (secondary)						
	Target Specification	CE v1.0					
					Need to check the shroud		
					around LS connector and		
					position of moounting holes		
					(don't seem to be in the same		
	Form Factor	CE v1.0 (standard) - 85 x	+/- 0.25mm error margin allowed	Pass	place as in HiKey)		Using 3D templates for the appropriate form-factor
				Validation			
Key	Documentation	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
					https://github.		
	Board Schematic published	Yes		Pass	com/sdrobertw/HelioX20		
	Board Reference Manual published				https://github.		
		Yes		Pass	com/sdrobertw/HelioX20		
	SoC technical reference published	Yes	Not published yet, but available and looks good	Pass			
				Validation			
Key	Memory and Storage	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description
	Onboard DRAM (GB)	2	CE: >= 0.5GB; EE: >= 1GB or DIMM	Pass		, , , , , , , , , , , , , , , , , , , ,	
	Additional mamorus as shake		22. 3.335, 22.7 - 100 01 011/11/1	Ignored			
	Additional memory sockets	microCDUC	CE: Mandahan	-			
	SD Card	microSDHC	CE: Mandatory	Pass	No option to boot from SD		
					card is assumed basing on lack		
					of information in the 'Getting		
	Boot from SD Card?	No		Pass	started'		
	Boot Holli 3D Card:	INO		F 033	Boot ROM mentioned in the		
					'Software User Manual' but no		
					specs about it. Marking as		
					Failed as there is no option to		
	Boot ROM	No	CE: optional, only if SD card is not bootable	Fail	boot from SD card		
	Boot ROM size (MB)	NO.	CE: 8MB (optional)	Ignored	DOOCTIONI SD Card		
				-			
	Onboard Flash storage		Optional	Pass			
	Onboard Flash storage size (GB)	8		Pass			
	SATA interface	None	Optional	Ignored			
				Validation			
Key	Networking	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Wifi protocol	802.11 g/n/ac		Pass			
	Bluetooth 4.0 LE			Pass			
				Validation			
Key	Debugging	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	UART 1 (LS expansion interface)	•	default mandatory UART	Pass	UART runs at 921600		
	UART 2 (LS expansion interface)		CE: optional	Pass	5, raiis ac >2 1000		
	OANT 2 (L3 expansion interrace)	103	CE. optional	1 033			
					Ausasi EDs available as the		
	4 User LEDs	Vaa		F-:I	4 user LEDs available on the		
				Fail	board, but in wrong place		
	WiFi LED			Fail	WiFi LED available but in wrong	•	
	BT LED			Fail	BT LED available but in wrong	P	
	JTAG	Yes	Optional	Pass	No headers		
				Validation			
Key	USB	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Port 1 - Host (Protocol)	USB 2.x		Pass			
	Port 1 - Host (Connector)			Pass			
	Port 2 - Host (Protocol)			Pass			
	` ;						
	Port 2 - Host (Connector)			Pass			
	Port 3 - Slave (Protocol)			Pass			
	Port 3 - Slave (Protocol) Port 3 - Slave (Connector) Port 4 - Host (Protocol)	micro-B (for slave-only)		Pass	not tested		

		1						
	Port 4 - Host (Connector)	Other			not tested			
				Validation				
Key	Display	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
Key	Display Type		CE: Audio support for at least 1 channel is mand		Lindro commencs	Timat Results (Linuto)	Validation reserves pescription	
	Display Connector		eziritedio sopporeror detease i andimetis mana	Pass				
	MIPI DSI port on HS expansion bus			Pass	not tested			
	MIPI DSI lanes			Pass	not tested			
	i i i i i i i i i i i i i i i i i i i			1 033	not tested			
				Validation				
Key	Camera	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
	MIPI CSI-2 ports	1	(Review) CE: Port 1 should be on CSI0 port (1-4 l	a				
				Validation				
Key	Audio	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
	Audio channels (BT)		CE: I2S/PCM channel should be provided on LS					
	Audio channels (Display)	2		Pass				
				Malidakiaa				
Kov	DC Power	Response	Comments	Validation	Linaro comments	Final Posulte (Linear)	Validation Test Description	
Key		DC Power Jack (8-18V)	Comments	Pass	Lillar o Comments	riliai Results (Liliaio)	validation rest description	
	Fower source	DC POWEI Jack (6-16V)		PdSS				
				Validation				
Key	Buttons / Switches / Jumpers	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
	Power on/off		Mandatory	Pass				
	Hard reset	Yes	Mandatory	Pass				
	Auto power up	Through Switch	Mandatory	Pass				
			Optional, to change mode of the OTG port		No possibility of changing USB			
	USB mode change	Through Switch	Optional, to change mode of the OTG port	Pass	mode from software			
V	Expansion header	Response	Comments	Validation	Linaro comments	Final Decules (Linear)	Validation Test Description	
Key	Low-speed expansion header		Signals to be exposed as per spec		Linaro comments	rinal Results (Linaro)	validation rest Description	
	High-speed expansion header		CE: mandatory	Pass Pass				
	riigii-speed expansion neader	res	CL. Hidhidacory	PdSS				
				Validation				
Key	Miscellaneous	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
,		•		Treserve (2.111e. o)	both 5V and 12V fans are	· met itesetts (2mers)	Talled on Table Description	
	External Fan	+5V		Pass	supported			
		_	_	Validation	-	- 1		
Key	Software	Response	Comments		Linaro comments	Final Results (Linaro)	Validation Test Description	
	Kernel git tree URL	nttps://github.com/heli	o-x20/linux/tree/helio-x20	Pass	Kaspal is not any of the list.			
					Kernel is not any of the list: - kernel.org latest "mainline"			
					or "stable" kernel			
					- The latest Google-supported			
	Kernel version				Android kernel version			
					- One of the last two kernel.			
		w2.10		F=:I	org LTS kernels (for example			
	Kernel compliance Level	v3.18	Level definitions as per spec	Fail Fail	Linaro LSK)			
	Kernet computatice Level	Oulei	Level definitions as her sher	ıdıl				
			According to the spec, it is recommended for					
			everything after SoC reset to be available as					
	All bootloader bits are open source							
	All bootloader bits are open source	No	open source					
		No	· ·		No git tree, but sources			
	Last-stage bootloader git tree URL	No http://builds.96boards.	· ·	Pass	No git tree, but sources available			
		No http://builds.96boards.v	· ·	Pass Pass Pass				

	Possibility to unbrick the board w/o special hardware			Fail	xlfash still doesn't work on Linux. Device doesn't appear as serial port. Possible to reboot to bootloader using adb		
	GPU acceleration	Needs userspace binary		Pass	libmali?		
	Camera ISP						
	DSP	NA					
	Multimedia	NA					
Key	Software Functionality	Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Boot to Graphical UI	Yes		Pass	only 1 core online		
	Graphics acceleration using SoC GPU	Yes		Pass			
ndard s	et of Distribution end user applications	Yes		Pass	browser works :)		
	Video playback support for media files			Pass	managed to get video playback		
edia file	via A2DP Bluetooth and display audio	Yes		Pass	managed to get HDMI audio wo		
	Networking using all onboard interfaces and USB ethernet interfaces	Partial		Fail	ETH-USB dongle doesn't work		
	Software power shutdown and reboot options	Yes		Fail	Power off works as reboot when dip switch is turned to 'auto on' position		
	Appropriate activity LEDs (e.g. Bluetooth, WiFi)	Yes		Pass			
	Additional storage is usable: eMMC (if fitted), SD, SATA, USB	Yes		Pass			