<table>
<thead>
<tr>
<th>Schematics</th>
<th>Revision</th>
<th>Date</th>
<th>Release Information</th>
</tr>
</thead>
<tbody>
<tr>
<td>SRT-PCB-MEZ-LTE-R3_1</td>
<td>A</td>
<td>Jan 12 2018</td>
<td>first version release</td>
</tr>
</tbody>
</table>

**Revision History**

01 Revision History
02 Block Diagram
03 DXF
04 Stuck Up
05 Interfacc_in_out
06 MODEM - 1
07 MODEM - 2
08 Sensors
09 MISC

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**Shiratech Solutions**

SRT-PCB-MEZ-LTE-R3_1

Drawn By Ishay Kokavka

Saturday, September 29, 2018
New cellular balcony

Block Diagram

Shiratech Solutions

SRT-PCB-MEZ-LTE-R3_1

Drawn By Ishay Kokavka

Saturday, September 29, 2018
number of layers 4
rf on top 50ohm (for 15mil)
board thickness 1.6mm

1) cs - signal
2) plan - gnd
3) plan - power
4) ps - signal
**LS EXPANSION CONNECTOR**

**from qualcom board**

**to next balcony**

**male connector (ps)**

**femal connector (cs)**

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**HS EXPANSION CONNECTOR**

**from qualcom board**

**to next balcony**

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**3.8V (2A Peak)**

Vout = 1.21(1+R2/R1)

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**3V3-REG - 0.8A**

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Shiratech Solutions

SRT-PCB-MEZ-LTE-R3_1

Drawn By Ishay Kokavka

Saturday, September 29, 2018
bno055

ADUX1020

adt7410

VL53L0CXV0DH_1

Shiratech Solutions

Sensors

SRT-PCB-MEZ-LTE-R3_1

Drawn By Ishay Kokavka

Sheet 8 of 9

Saturday, September 29, 2018
LEFT FOR MISC AND FUTURE REMARKS